

IN THE CLAIMS

Please amend the claims as follows:

Listing of Claims:

Claims 1-21 (Canceled).

22. (Previously Presented) A variable gain circuit,  
comprising:

a first amplifier with high gain having a first amplifier input and a first amplifier output, and being gain-controlled by a first gain control signal; and

a second amplifier with low gain having a second amplifier input and a second amplifier output, and being gain-controlled by a second gain control signal,

wherein direction of a gain change of the first amplifier with respect to a change of the first gain control signal, and direction of a gain change of the second amplifier with respect to a change of the second gain control signal are set in reverse to each other, and the first gain control signal and the second gain control signal are in common use,

wherein an input signal is supplied in common to the first and the second amplifiers by coupling the first amplifier input and the second amplifier input with each other, and the output

signal of the first amplifier and the output signal of the second amplifier are additionally combined by coupling the first amplifier output and the second amplifier output with each other, and

wherein the first amplifier has a function to turn on and off an output in response to a mode switching signal.

23. (Previously Presented) The variable gain circuit according to claim 22, wherein the first amplifier is comprising a first fixed gain amplifier of voltage input current output type, and a first shunt circuit for shunting an output current of the first fixed gain amplifier to two current output terminals at a shunt ratio according to the first gain control signal, and the second amplifier is comprising a second fixed gain amplifier of voltage input current output type, and a second shunt circuit for shunting an output current of the second fixed gain amplifier to two current output terminals at a shunt ratio according to the second gain control signal,

wherein an input terminal of the first fixed gain amplifier and an input terminal of the second fixed gain amplifier form the first amplifier input and the second amplifier input, respectively, and either of the current output terminals of the

first shunt circuit and either of the current output terminals of the second shunt circuit form the first amplifier output and the second amplifier output, respectively.

24. (Previously Presented) The variable gain circuit according to claim 22, wherein the first amplifier input and the second amplifier input are differential inputs, respectively.

25. (Previously Presented) The variable gain circuit according to claim 24, wherein the first amplifier output and the second amplifier output are differential outputs, respectively.

26. (Previously Presented) A variable gain circuit, comprising:

a first amplifier with high gain having a first amplifier input and a first amplifier output, and being gain-controlled by a first gain control signal;

a second amplifier with low gain having a second amplifier input and a second amplifier output, and being gain-controlled by a second gain control signal; and

a third amplifier having a third amplifier input and a third amplifier output, and being gain-controlled by a third gain control signal,

wherein an input signal is supplied in common to the first and the second amplifiers by coupling the first amplifier input and the second amplifier input with each other, and the output signal of the first amplifier and the output signal of the second amplifier are additionally combined by coupling the first amplifier output and the second amplifier output with each other, wherein the third amplifier input is coupled with the first amplifier output and the second amplifier output, and the first amplifier has a function to turn on and off an output in response to a mode switching signal.

27. (Previously Presented) The variable gain circuit according to claim 26, comprising a gain correction circuit for correcting an amount of gain change resulting from the output of the first amplifier being cut off, by changing a gain of the third amplifier using the third gain control signal, at the same time when the output of the first amplifier is cut off, wherein an amplitude fluctuation in the output of the third amplifier is prevented by the gain correction circuit.

28. (Currently Amended) A variable gain circuit, comprising:  
a first amplifier with high gain having a first amplifier input and a first amplifier output, and being gain-controlled by a first gain control signal;

a second amplifier with low gain having a second amplifier input and a second amplifier output, and being gain-controlled by a second gain control signal; and

a [[,]] gain control signal converting circuit for creating the first gain control signal and the second gain control signal from a fourth gain control signal,

wherein an input signal is supplied in common to the first and the second amplifiers by coupling the first amplifier input and the second amplifier input with each other, and the output signal of the first amplifier and the output signal of the second amplifier are additionally combined by coupling the first amplifier output and the second amplifier output with each other, and

wherein the first amplifier has a function to turn on and off an output in response to a mode switching signal.

29. (Previously Presented) The variable gain circuit according to claim 26, comprising a gain control signal

converting circuit for creating the first gain control signal, the second gain control signal, and the third gain control signal from the fourth gain control signal.

30. (Previously Presented) The variable gain circuit according to claim 29, comprising a gain correction circuit for correcting, in conjunction with the output of the first amplifier being cut off by the mode switching signal, an amount of gain change resulting from the output of the first amplifier being cut off, by shifting the third gain control signal, wherein an amplitude fluctuation in the output of the third amplifier is prevented by the gain correction circuit.

31. (Previously Presented) The variable gain circuit according to claim 30, comprising a detection circuit for outputting the mode switching signal by comparing the fourth gain control signal with a reference signal.

32. (Previously Presented) The variable gain circuit according to claim 30, comprising a detection circuit for outputting the mode switching signal by comparing an output

amplitude of the third gain control signal with a reference signal.

33. (Previously Presented) The variable gain circuit according to claim 30, wherein at a subsequent stage of the output of the third amplifier, an amplifier or a mixer circuit is connected, and the variable gain circuit comprises a detection circuit for outputting the mode switching signal by comparing an amplitude of an output signal of the amplifier or the mixer circuit with a reference signal.

34. (Previously Presented) The variable gain circuit according to claim 30, comprising a detection circuit for outputting the mode switching signal by comparing an input amplitude of the first amplifier with a reference signal.

35. (Previously Presented) The variable gain circuit according to claim 31, wherein the detection circuit, using a clock signal for detection of the fourth gain control signal, has a function to perform the detection at every certain timing.

36. (Previously Presented) The variable gain circuit according to claim 32, wherein the detection circuit, using a

clock signal for detection of the output amplitude of the third amplifier, has a function to perform the detection at every certain timing.

37. (Previously Presented) The variable gain circuit according to claim 33, wherein the detection circuit, using a clock signal for detection of the amplitude of the output signal of the amplifier or the mixer circuit, has a function to perform the detection at every certain timing.

38. (Previously Presented) The variable gain circuit according to claim 34, wherein the detection circuit, using a clock signal for detection of the input signal of the first amplifier, has a function to perform the detection at every certain timing.

39. (Previously Presented) The variable gain circuit according to claim 31, comprising a mode switching status circuit for implementing, by a switching enabling signal, an active state of allowing the ON/OFF switching operation for the first amplifier and the control for the gain correction circuit in conjunction with it, and a sleep state of prohibiting the operation.



40. (Previously Presented) A variable gain circuit, comprising:

a first amplifier with high gain having a first amplifier input and a first amplifier output, and being gain-controlled by a first gain control signal;

a second amplifier with low gain having a second amplifier input and a second amplifier output, and being gain-controlled by a second gain control signal; and

an amplifier or an attenuator having a gain varying function, which is arranged at a preceding stage of the inputs of the first amplifier and the second amplifier,

wherein an input signal is supplied in common to the first and the second amplifiers by coupling the first amplifier input and the second amplifier input with each other, and the output signal of the first amplifier and the output signal of the second amplifier are additionally combined by coupling the first amplifier output and the second amplifier output with each other, and

wherein the first amplifier has a function to turn on and off an output in response to a mode switching signal.

41. (Previously Presented) The variable gain circuit according to claim 40, wherein the amplifier or the attenuator having the gain varying function changes a gain simultaneously with generation of the mode switching signal.